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PTO/SB/08A (10-96)

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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1 of 2

Complete if Known 109720541

Application Number	(not yet assigned)
Filing Date	1-26-2001
First Named Inventor	BANERJEE, et al.
Group Art Unit	2815 (not yet assigned)
Examiner Name	(not yet assigned)

Number NWU - Pooj

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U.S. PATENT DOCUMENTS

Christopher

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Date Considered

1/28/03

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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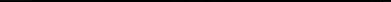
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT			
(use as many sheets as necessary)			
Sheet	2	of	2
		Attorney Docket Number NWU-PO01	
Application Number		(not yet assigned)	
Filing Date		1-26-2001	
First Named Inventor		DANERJEE, et al.	
Group Art Unit		2815 (not yet assigned)	
Examiner Name		(not yet assigned)	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

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Examiner Initials'	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
C.C.	1.	P.BANERJEE, N.SHENOY, A.CHOUDHARY, S.HAUCK, C.BACHMANN, M.HALDAR, P.JOISHA, A.JONES, A.KANHARE, A.NAYAK, S.PERIYACHERI, M.WALKDEN, D.ZARGATSKY; A MATLAB Compiler For Distributed, Heterogeneous, Reconfigurable Computing Systems, Proc. IEEE Symposium On Field-Programmable Custom Computing Machines, FCCM'00, April 2000, 10pages, Electrical and Computer Engineering; Northwestern University, Evanston, IL-60208 - USA
C.C.	2.	MALAY HALDAR, ANSHUMAN NAYAK, ALOK CHOUDHARY and PRITH BANERJEE: AUTOMATED SYNTHESIS OF PIPELINED DESIGNS ON FPGAS FOR SIGNAL AND IMAGE PROCESSING APPLICATIONS DESCRIBED IN MATLAB; Proc. of ASIA PACIFIC DESIGN AUTOMATION CONFERENCE (ASP-DAC) January, 2001; 4 pages; MACH Design Systems, Inc. Schaumberg, IL USA
C.C.	3.	MALAY HALDAR, ANSHUMAN NAYAK, ALOK CHOUDHARY and PRITH BANERJEE: FPGA HARDWARE SYNTHESIS FROM MATLAB; VLSI DESIGN CONFERENCE, BANGALORE, INDIA; January 2001, 8 pages, Center For Parallel and Distributed Computing, Northwestern University ,Evanston, IL 60208-3118 USA

Examiner Signature		Date Considered	1/28/03
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